

AMENDMENTS TO THE CLAIMS

Please **AMEND** claims 21-24 as shown below.

Please **ADD** claims 25-31 as shown below.

The following is a complete list of all claims in this application.

1-20 (Cancelled)

21. (Currently Amended) A thin film transistor (TFT) array panel for a liquid crystal display, comprising:

a gate line ~~formed in a first direction;~~

a data line ~~formed in a second direction, wherein the data line intersects~~ intersecting the gate line ~~is insulated from the gate line;~~

a pixel region defined by the intersection of the data line and the gate line;

a pixel electrode formed in a the pixel region ~~defined by an intersection of the gate line and the data line;~~ and

a conductive pattern ~~that is connected to the data line and shaped like~~ having a ladder shape;

~~wherein the first direction is not parallel with the second direction.~~

22. (Currently Amended) The ~~thin film transistor~~ TFT array panel of claim 21, further comprising: a storage wire including a storage electrode line, ~~wherein the storage electrode line connects the storage electrode of a neighboring pixel, and wherein the pixel electrode forms a storage capacitance by overlapping the storage wire and receives image signals through the data line.~~

23. (Currently Amended) The ~~thin film transistor~~ TFT array panel of claim 21, further comprising: a passivation layer formed between the pixel electrode and the data line.

24. (Currently Amended) A thin film transistor (TFT) array panel for a liquid crystal display, comprising:

a gate line ~~formed in a first direction;~~

a common wire including a common electrode line ~~that is connected to a common electrode;~~

a data line ~~formed in a second direction, wherein the data wire intersects~~ intersecting the gate line ~~and is insulated from the gate line;~~

a pixel region defined by the intersection of the gate line and the data line;

a pixel electrode formed in a the pixel region and overlapping the common electrode ~~defined by an intersection of the gate line and the data line, wherein the pixel electrode forms a storage capacitance by overlapping the common wire and receives image signals through the data line; and~~

a conductive pattern ~~that is connected to the data line and is shaped like~~ having a ladder shape,

~~wherein the first direction is not parallel with the second direction.~~

25. (New) The TFT array panel of claim 21, wherein the conductive pattern comprises:

a first bar extending along the data line;

a second bar spaced apart from the first bar and extending along the data line;

a first branch bridging the first bar and the second bar; and

a second branch spaced apart from the first branch and bridging the first bar and the second bar.

26. (New) The TFT array panel of claim 25, wherein the data line is arranged between the first bar and the second bar.

27. (New) The TFT array panel of claim 25, wherein the first branch and the second branch are substantially parallel to the gate line.

28. (New) The TFT array panel of claim 25, wherein the data line has a disconnected portion between the first branch and the second branch.

29. (New) The TFT array panel of claim 25, wherein the first branch and the second branch are electrically connected to the data line.

30. (New) The TFT array panel of claim 25, wherein the data line overlaps the first branch and the second branch.

31. (New) The TFT array panel of claim 22, wherein the conductive pattern and the storage wire are formed of the same material.